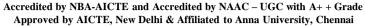


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# 19EE305 / EMBEDDED SYSTEMS III YEAR / VI SEMESTER

### **UNIT-I: INTRODUCTION TO EMBEDDED SYSTEMS**

### DESIGN PROCESS





# **Design Process in Embedded System**



- Concepts used during Design process
  - Abstraction
  - Hardware and Software Architectures
  - Extra Functional Properties
  - System Related family of Designs Modular Design
  - Mapping
  - User Interfaces Design Refinements





# **Design Process in Embedded System**

Development



**Process** Model / Analyse Requirements of System Design Data Structure, Software 2 Architecture, Interfaces and Algorithms One Linear Life Sequence Cycle Implementation 3 of Design Test Internal logic and External functions





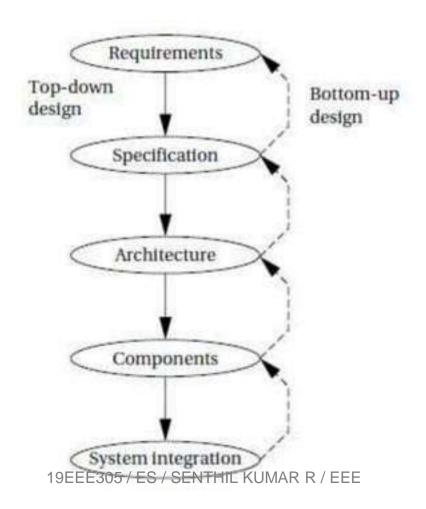
SIS

- Power Dissipation
- Performance
- Process Deadlines
- User Interfaces
- Size
- Engineering Cost
- Manufacturing Cost
- Flexibility
- Prototype Development Time
- Time to Market
- System and User Safety
- Maintenance<sub>19EEE305 / ES / SENTHIL KUMAR R / EEE</sub>

# **Design Process in Embedded System**



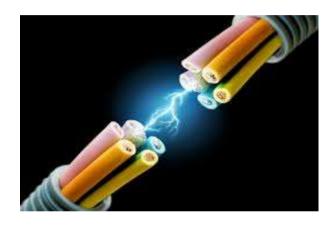
# **Abstraction of Steps in Design Process**





## RECAP....





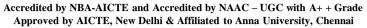
## ...THANK YOU





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### **UNIT-I: INTRODUCTION TO EMBEDDED SYSTEMS**

## STRUCTURAL UNITS

