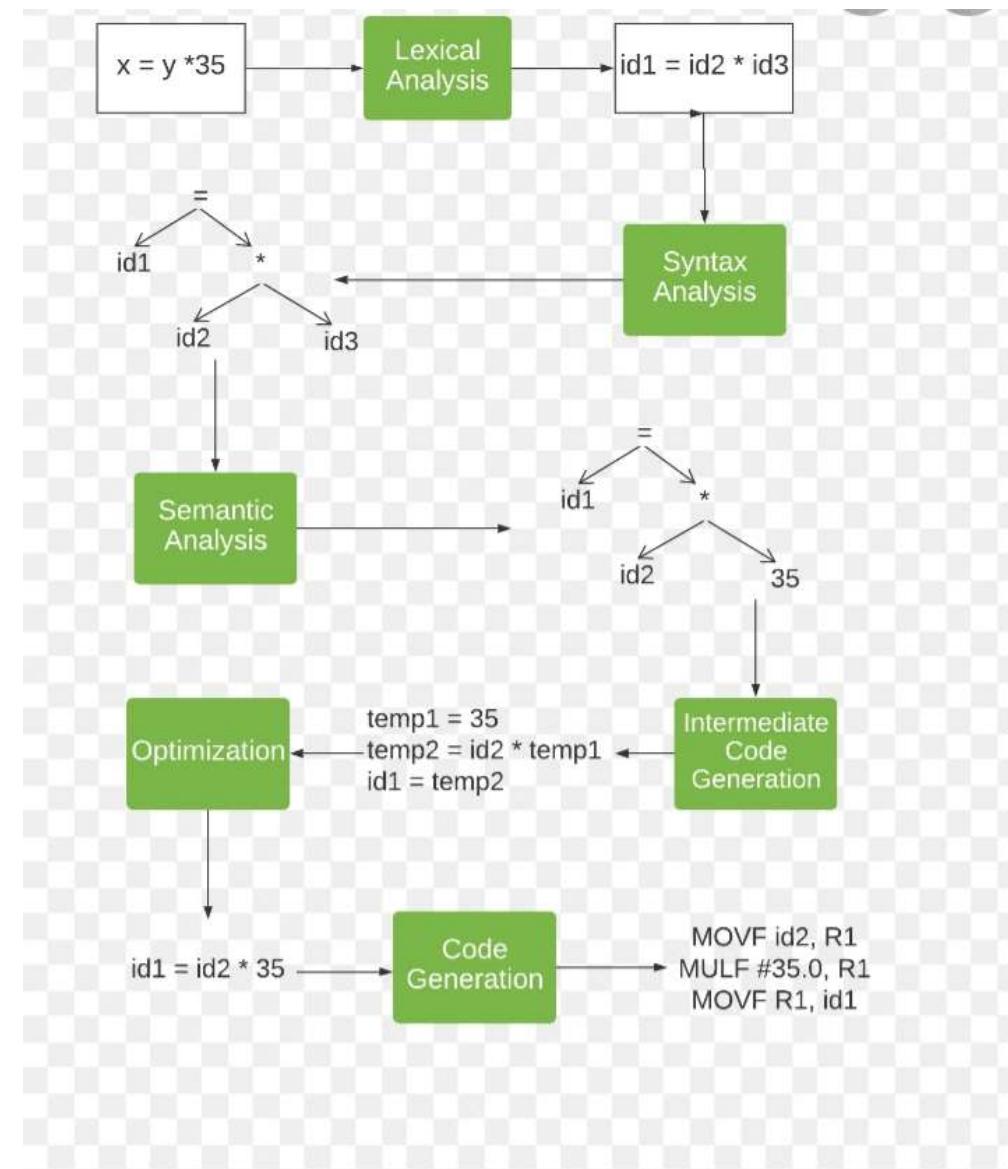




# Example for phases of compiler





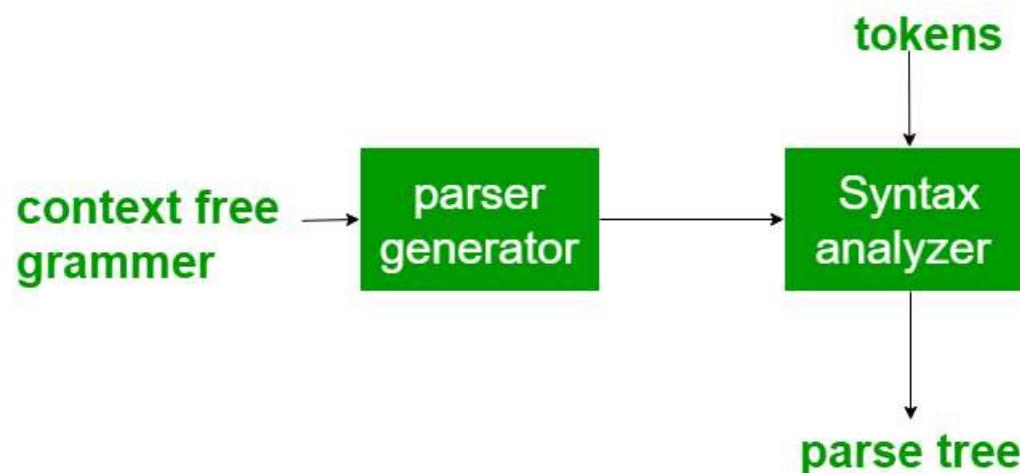
# Compiler Construction Tool

- Compiler tool → implementing the phases of compiler
- Creation of compiler
- Tools
  - Parser generator
  - Scanner generator
  - Syntax-directed Translation Engines
  - Automatic code generator
  - Data-Flow Analysis Engine
  - Compiler Construction Toolkits



# Parser Generator

- Input → grammatical description of a programming language
- Output → Syntax Analyzers
- Example: PIC, EQM



Syntax Tree Generator

Help License Please send suggestions to mail@mshang.ca

[S [NP Miles] [VP [V ate] [NP\* all the hot dogs]]]

Font style:  Serif  Sans-Serif  Monospace

Font size:

Vertical spacing:

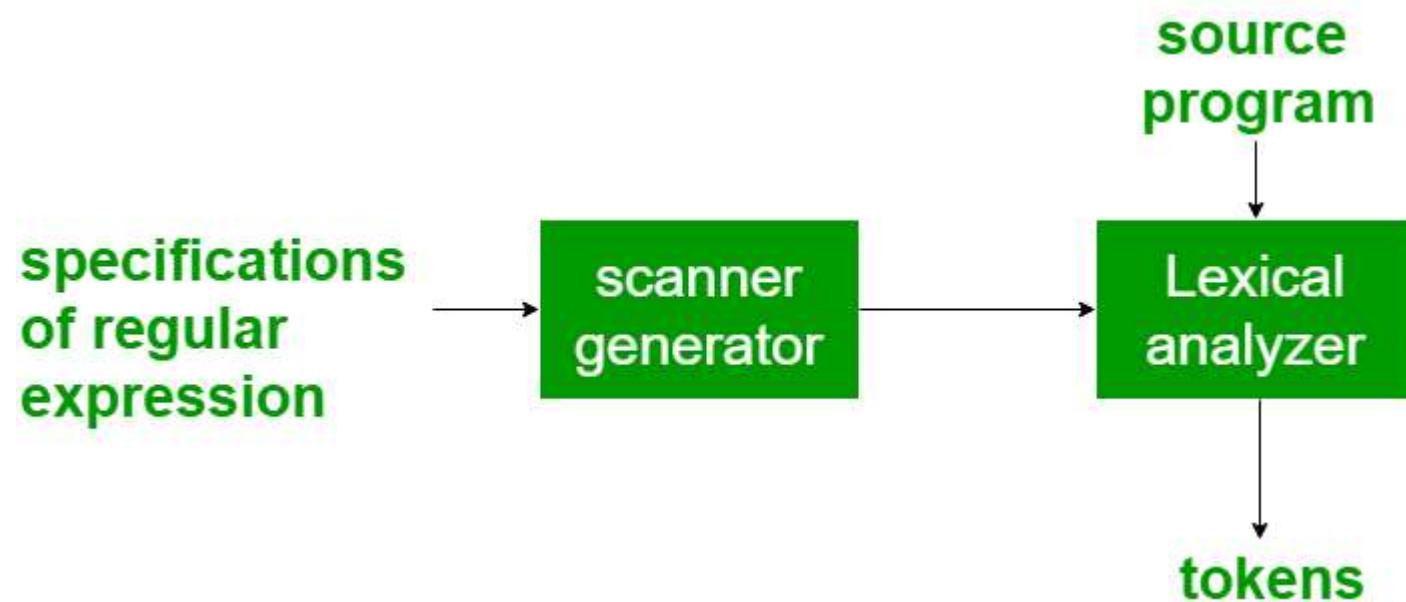
Horizontal spacing:

```
graph TD; S --- NP1[NP]; S --- VP1[VP]; NP1 --- Miles[Miles]; VP1 --- V1[V]; VP1 --- NP2[NP]; V1 --- ate[ate]; NP2 --- NP3[NP]; NP3 --- NP4[NP]; NP3 --- NP5[NP]; NP4 --- all[all]; NP4 --- the[the]; NP4 --- hot[hot]; NP4 --- dogs[dogs];
```



# Scanner Generator

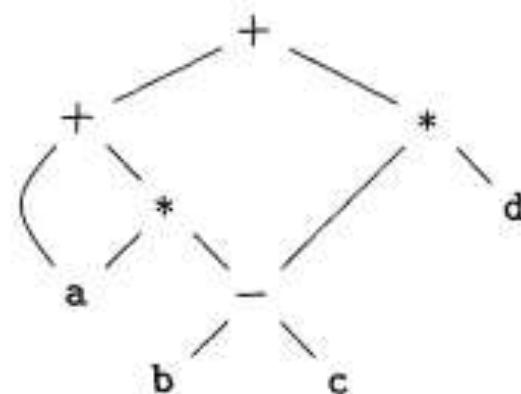
- **Input:** Regular expression description of the tokens of a language  
**Output:** Lexical analyzers.





# Syntax-directed Translation Engines

- Intermediate code (Three Address Format)
- Input: Parse tree.
- Output: Intermediate code.



$$\begin{aligned}t_1 &= b - c \\t_2 &= a * t_1 \\t_3 &= a + t_2 \\t_4 &= t_1 * d \\t_5 &= t_3 + t_4\end{aligned}$$

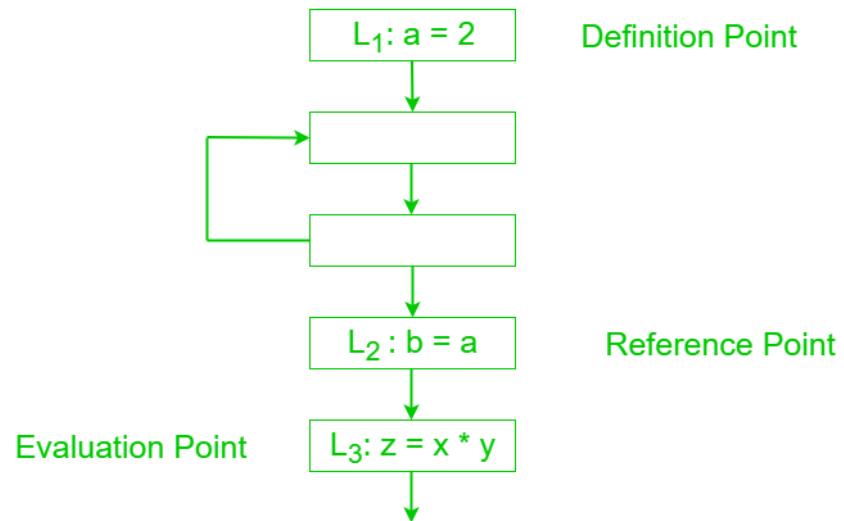


# Automatic code generator

- Input: Intermediate language.
- Output: Machine language.
- Rules → Target code

# Data-Flow Analysis Engine

- Key part of code optimization
- Information – value transferred from one part to other part of the program





# Compiler Construction Toolkits

- Integrated set of routines – phases of compiler

