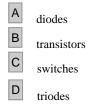
# **VLSI MCQ**

#### VLSI is an acronym for \_\_\_\_?

- А Very large Scale Integration
- В Varying large Scale Integration
- С Varying large Scale Integrity
- D None of the above

## Answer : A

#### which below option is used form integrated circuit VLSI technology?



## Answer: B

#### MOSFETs possess \_\_\_\_\_.

- A Low packing density
- в High packing density
- С Poor packing density
- D None of the above

### Answer: B

### Which among the following is a process of transforming design entry information of the circuit into a set of logic equations?



# Answer : C

#### Integrated chip with lesser than 10 transistors is known as \_\_\_\_\_.

- А
- Small-scale integration В
- Medium-scale integration
- С Large-scale integration
- D Very large-scale integration

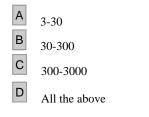
### Answer : A

\_ is the fundamental architecture block or element of a target PLD.

Α	System Partitioning
в	Pre-layout Simulation
С	Logic cell
D	Post-layout Simulation

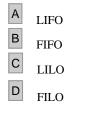
# Answer : C

### How many gates are present on each chip for Medium scale integration?



## Answer : B

### Logic design of VLSI is used\_\_\_\_\_



# Answer : B

### In VLSI design, which process deals with the determination of resistance & capacitance of interconnections?

A Floorplanning
B Extraction
C Placement & Routing
D Testing

### Answer : B

### Which type of MOSFET exhibits no current at zero gate voltage?

- A Depletion MOSFET
- B Enhancement MOSFET
- C Both A & B
- D None of the above

# Answer : B

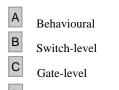
### The metal preferred in VLSI for contacting is \_\_\_\_\_.

- A Antimony
- B Aluminum
- C Germanium



# Answer: B

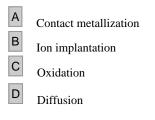
## Which type of simulation mode is used to check the timing performance of a design?



D Transistor-level

# Answer : C

#### Doping can also be done by using \_\_\_\_\_.



## Answer: B

In the simulation process, which step specifies the conversion of VHDL intermediate code so that it can be used by the simulator?

A Compilation В Elaboration С Initialization D Execution

### Answer : B

### MOSFET is a \_\_\_\_\_ device.

- A Uni-polar В Bipolar С Complementary
- D
- Supplementary

## Answer : A

### The VLSI design flow starts with:



RTL synthesis

Library mapping С

Product specifications D

None of the above

Answer : C

# Register transfer level description specifies all of the registers in a design & \_\_\_\_\_ logic between them.

- A SequentialB Combination
- <sup>B</sup> Combinational
- C Both a and b
- D None of the above

## Answer : B

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#### Which model is used for scaling?

Α	constant electric and voltage scaling

- B constant current model
- C constant voltage scaling
- D constant electric scaling

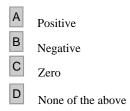
## Answer : A

#### Which is the high level representation of VLSI design?

- A Logic design
- B Functional design
- C HDL program
- D Problem statement

## Answer : D

# Depletion mode n MOSFETs have \_\_\_\_\_ threshold voltage.



Answer : B