

PIN diode:

It is a type of photo detector.

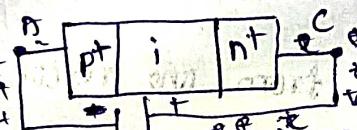
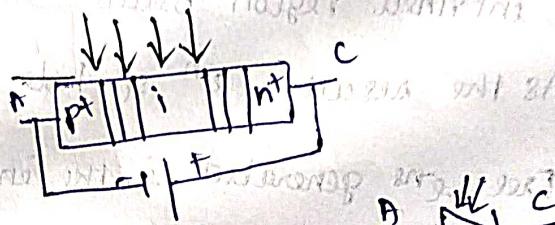
* It converts light energy into electrical energy.

* PIN diodes are specially operate in reverse bias condition.

* PIN diode is very sensitive to light so when

light falls on the photodiode it easily converts light into electric current.

Symbol:



Here P^+ \rightarrow heavily doped p-type semiconductor.

$I \rightarrow$ undoped intrinsic semiconductor.
(highly resistive)

The intrinsic semiconductor is sandwiched between p-type & n-type semiconductors to avoid breakdown.

Working:

PIN diode is made up of p-i-n region separated by a highly resistive intrinsic layer. This is because intrinsic layer is placed between p & n because region to the width of the depletion region.

p & n are heavily doped, therefore, p-i-n region of PIN diode has large number of charge carriers to carry electric current.

Intrinsic \rightarrow undoped \rightarrow It doesn't have charge carriers to conduct electric current.

under reverse bias condition, majority charge carriers in n type & p ^{region} move away from the junction.

As the result, the width of the depletion region becomes very wide.

When light ($\lambda \approx 0.5\text{ }\mu\text{m}$) photon is applied to the PIN diode, the most part of the energy is observed by the intrinsic region because of wide depletion width. As the result, e^-e^+ hole pairs are created.

Free e^-s , generated in the intrinsic region moves towards N side & hole moves towards P side. Free e^-s moves from one region to another region carries electric current.

When forward bias is applied to the PIN diode it behaves as a "resistor".

When reverse bias is applied to the PIN diode it behaves as a "capacitance".

Advantages:

- (i) wide band width
- (ii) high response speed
- (iii) high sensitivity to light
- (iv) low sensitivity to temp.
- (v) low cost
- (vi) small size
- (vii) long lifetime

disadvantage:

- * PIN diode should always be operated in reverse bias condition.

* Reverse bias voltage is low

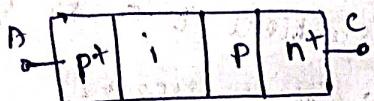
Applications:

- * in signal isolators.
- * used as radio frequency switching
- * used as photo detector as photodetector
- * used in optical fibre
- * In medical treatment (MRI)
- * High voltage rectifier.

Avalanche photo diode:

It is the photo detector. It converts light energy into electrical energy. It operates in reverse bias condition.

symbol:



It is the four layer device.

p^+ → heavily doped { There are very large no. of charge carriers
 n^+ → heavily doped and its resistivity is low

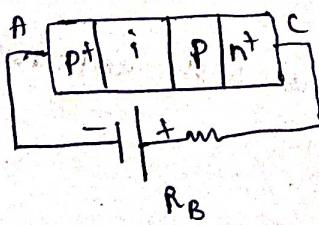
i → intrinsic layer.

p → lightly doped region.

p^+ acts like anode

n^+ " " cathode

APD works like photo detector, when it operate in reverse bias condition.



when the light is applied, light enters to

p^+ region to i region, the photons are interact with atoms in the intrinsic region.

Because of interaction the e^-n or hole pairs are generated in intrinsic region.

In Intrinsic region the e^-n hole pairs experiences high electric field because of reverse bias.

Because of this high electric field, the e^- moves toward p region & holes moves towards n⁺ region.

e^- drifts to p region will again experiences very high electric field b/w p & n⁺ region are accelerated. e^- s are accelerated due to high EF in p-n junction. They will get sufficient energy to emit secondary e^- s in its path.

These secondary e^- s has sufficient KE to generate one more e^- & hole pairs if it is repeated.

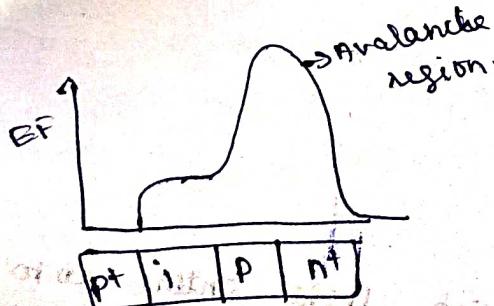
The process of generating more number of e^- & hole pair is called "Impact Ionization". Because of impact ionization multiple charge carriers are produced.

Only the e^- s will participate in the process of impact ionisation.

Every e^- & hole pair has ability to generate the m no. of e^- & hole pairs.

m → multiplication factor.

$$I_p = q N_e^m \Rightarrow I_p \rightarrow \text{Photo current}$$



It has high internal gain

Application:

(i) high sensitivity (detect light in low intensity)

(ii) very high response time.

(iii) ability to generate very high current.

(iv) they have internal current gain mechanism

disadvantages: very large reverse bias voltage required.

It requires high voltages to operate.

a DIP is not linear.